

UNITED STATES PATENT AND TRADEMARK OFFICE

plicant: Steve H. Weissinge Serial No.: 10/076,975

: February 15, 2002

Examiner: Unknown

OBTAINING CYCLIC REDUNDANCY CODES

Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Art Unit : Unknown

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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Sheet	1	of	1

Substitute Form Pro 1449 (Modified)

U.S. Department of Commerce Patent and Trademark Office

Attorney's Docket No. 10559-576001

Applicant

Application No. 10/076,975

Information Disclosure Statement by Applicant (Use several sheets if necessary)

Steve H. Weissinger

Group Art Unit

(37 CFR §1.98(b))

Filing Date February 15, 2002

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF				REC	EIVE	
	AG				1	0 2 2002	
	АН						
	AI				Technolo	gy Center 2	100
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or			Trans	slation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AL						-	
	AM							
	AN							
	AO							
	AP							

	Other Documents (include Author, Title, Date, and Place of Publication)					
Examiner Initial	Desig. ID	Document				
	AQ	Glaise et al., Fast CRC Calculation, 1993, IEEE				
	AR	Feldmeier, Fast Software Implementation of Error Detection Codes, 12/1995, IEEE/ACM Transactions on Networking, Vol. 3, No. 6				
	AS	Glaise, A two-step computation of cyclic redundancy code CRC-32 for ATM networks, 11/1997, IBM J. Res. Develop., Vol. 41 No. 6				
	AT	Roginsky et al., Efficient computation of packet CRC from partial CRCs with application to the Cells-In-Frames protocol, 1998, Computer Communications 21, pp. 654-661				
	AU	Joshi et al., A New Parallel Algorithm for CRC Generation, 2000, IEEE, pp. 1764-1768				

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if no	t in conformance and not considered. Include conv of this form with
next communication to applicant.	this contained and not considered. History of this form with